

Faculty Profile

Faculty Name: Mr. K Kishore Kumar
Department: ECE

Faculty Photo:



Faculty Description:

Mr. K Kishore Kumar received M.Tech from SMCET, JNTUH, Hyderabad in year 2012; B.Tech from Sri Kottam Tulasi Reddy Memorial College of Engineering, Kondair, JNTU Hyderabad in year 2008. He has eleven years of teaching and industry experience. He teaches Basic Electronics Subjects, Digital Electronics and Programming languages like c and python. He believes in seeking and realizing truth in day-to-day activities. He encourages students to earn knowledge of both technical and non-technical fields. Areas of interest includes Analog & Digital Circuits, VLSI Technology, Embedded System Design, Microprocessor & Microcontrollers.

Qualification:

Qualification	Institution	Year
B. Tech (ECE)	SKTRMCE, JNTU, Hyderabad.	2008
M.Tech (ECE)	SMCET, JNTU, Hyderabad.	2012

Experience:

Designation	Institution	From	To
Assistant Professor	Dr. KVSRIIT, Kurnool	01.11.2012	Till Date
Assistant Professor	K.K.I.T, Kurnool	01.06.2008	01.06.2010

Awards / Achievement:

- ✓ A *JNTUA RATIFIED* faculty.

Responsibilities Tab:

- HOD, KKIT, Chinnatekur, Kurnool district (2008-2010).
- HOD, KVSRRIT, Dupadu, Kurnool (2012-2013).
- i/c Examination Section., Dr KVSRRIT, Kurnool (2013-2015).
- i/c AICTE, KVSRRIT, Kurnool (2015-2018).
- HOD Diploma, ECE Dept., Dr KVSRRIT(2020-2021).

Responsibilities:

- ✓ Working as a faculty
The following responsibilities are carried out as a faculty
 - Teaching regular Course Work
 - Mentoring the Students
 - Guiding the Junior Faculty
 - Continues research

Teaching Tab:**Under Graduate Courses:**

- Electronic Devices and Circuits
- Electronic Circuit Analysis
- Pulse and Digital Circuits
- Switching theory and logic design
- Microprocessors and Microcontrollers
- Optical fibre communications
- Digital Image processing
- VLSI design
- Control systems Engineering
- Computer Organization & Architecture
- Computer Organization
- Analog Electronic Circuits
- Digital IC Applications
- Signals & Systems
- Digital communication

Post Graduate Courses:

- Hardware Description Language
- Research Methodology
- Embedded systems
- Structural digital system design

Research Tab:

Research Interest

- Low Power VLSI Design
- Analog & Mixed Signal design
- Wireless Communications
- Analog, Digital & Data Communications
- Digital Electronics
- Machine Learning

Publications tab:

Text Book Authored:

Conferences:

1. Published paper in National Conference in BPC college, Kerala on Modern Trends in Electronic communication and signal processing, entitled as, “*Histogram Modification based Reversible Data Hiding*”.
2. **Shaik Mahaboob Subahan, K. Kishore Kumar**, “Design of Low Power and high Speed Multiplier With Dynamic Range Detector”, National Conference on Innovative Research in Engineering & Technology (NCIRET 2015), April 2015. ISBN:978-I-634I5-634-9.
3. **K.Kishore Kumar, Shaik Mahaboob Subahan**, “Histogram Modification Based reversible Data Hiding Algorithm”, National Conference on Innovative Research in Engineering & Technology (NCIRET 2015), April 2015. 978-I-634I5-634-9.

Journals:

1. **K. Kishore Kumar, R Swarna Sri**,” Aging Aware Dependable Multiplier with Self Evolving Hold Logic Using Verilog HDL”, Journal of Engineering Sciences, ISSN:0377-9254, volume10, issue 8 Aug 2019.

2. **K. Kishore Kumar, P. Servesh**, “VLSI design and implementation of area efficient architecture for Montgomery modular multiplication”, International Journal of Research (IJR) Vol.7, Issue 7, Dec 2018. (ISSN : 2236-6124 (online)).
3. **S. Nelofer, K. Kishore Kumar**, “Detection of Single Error and Double Error correction codes with fast decoding of critical bits”, International Journal of Management Technology and Engineering (IJMTE) Vol.8, Issue 8, Aug 2018. (ISSN : 2249-7455 (online))
4. **S. Sirisha, K. Kishore Kumar**, “Design and implementation of encryption module for AES core using verilog”, International Journal of Creative Research Thoughts (IJCRT) Vol.6, Issue 1, Feb 2018. (ISSN : 2320-2882 (online))
5. **B Reshma Parveen, K Kishore Kumar**, “Implementation of Effective QPSK Modulator Based On FPGA”, International Journal of VLSI Design and communication Systems, ISSN 2322-0929 Vol.05, Issue.12, December-2017, Pages:1269-1272.
6. **Fazia Kousar, K Kishore Kumar**, “Design and study of low power high speed full adder using GDI technique”, International Journal & Magazine of Engineering, Technology, Management and Research. ISSN No: 2348 – 4845 (Online) VOLUME 4, ISSUE 8, AUGUST 2017.

Faculty Development Programs and Workshops Attended:

1. Attended a One week National Faculty Development Program on **Artificial Intelligence** from 24/05/2020 – 29/05/2020 organized by Chalapathi Institute of Technology, Guntur.
2. Attended a 5 Day Faculty Development Program on **Machine Learning** from 01/06/2020 – 05/06/2020 organized by Lakki Reddy Bali Reddy College of Engineering, Hyderabad.
3. Attended a One week Faculty Development Program on **Applied Machine Learning & Deep Learning** from 15/06/2020 – 20/06/2020 organized by APSSDC, Govt of Andhra Pradesh.
4. Attended a Two Day Faculty Development Program on Matlab and Simulink from 10/06/2020 – 11/06/2020 organized by APSSDC, Govt of Andhra Pradesh.
5. Attended a One week National Workshop on **Advanced Python programming** from 18/06/2020 – 24/06/2020 organized by Chalapathi Institute of Technology, Guntur.
6. Attended a Two Day National Level Workshop on Machine Learning for Human-Robot Collaborate and AI Interface from 22/11/2019 – 23/11/2019 organized by Malla Reddy College of Engineering and Technology, Hyderabad.
7. Attended One Week Faculty Development Program on Machine Learning and Deep Learning from 29/04/2019 – 04/05/2019 organized by Malla Reddy College of Engineering and Technology.
8. Embedded system design workshop conducted by Edgate Technology Sep 2018.
9. Ethical Hacking workshop conducted by KRIYON DIGITAL SECURITIES (IIT, Kharagpur)
10. Attended a two day training workshop on Quality Improvement Program on Instructional Strategies from 2/07/09 – 4/07/09 organized by NITTTR & conducted by SKTRMCE.
11. Hibernate and Spring Certification Program conducted by Tavant Technologiesn Banglaore, 2007.

Youtube Channel:

Blog Tab:

<https://kishorekumarbooks.blogspot.com/>